



Expedited Procedure
Group No. 2816

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rajiv Jain and Richard J. Wong
Assignee: QuickLogic Corporation
Title: Method of Programming an Antifuse
Serial No.: 09/887,834 Filing Date: June 22, 2001
Examiner: T. Cunningham Group Art Unit: 2816
Docket No.: M-9351 US

San Jose, California
November 26, 2002

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Washington, D. C. 20231

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RESPONSE TO FINAL OFFICE ACTION UNDE RULE 116

Sir:

This is a response to the August 27, 2002 Final Office Action, which has a statutorily shortened period that ends on November 27, 2002. Please enter the following amendments before taking action on the merits of the above-referenced application. These amendments are to matters of form and to put this case in condition for allowance, and hence entitled to entry under Rule 116.

In the Specification

Please replace the paragraph starting on page 8, line 11 and ending on page 8, line 18, with the following rewritten paragraph:

Fig. 5 shows programming pulses that include a plurality of current limited prepulses that may be used to program antifuse 100 in accordance with another embodiment of the present invention. As shown in Fig. 5, an initial prepulse 202, which is at full voltage and is current limited, is used to move the conductive filament 113 into amorphous silicon 112. A second prepulse 210, which has opposite polarity, follows prepulse 202. The second prepulse

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